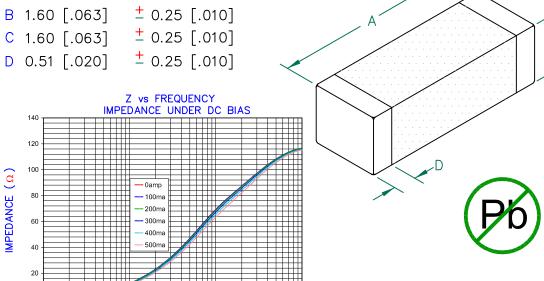
# LI1806E800R-10

# **UNCONTROLLED**

## PHYSICAL DIMENSIONS:

A 4.50 [.177] ± 0.25 [.010]

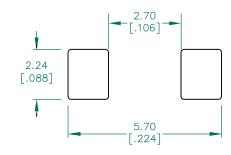


ELECTRICAL CHARACTERISTICS:							
Z @ 100MHz (Ω)		DCR ( $\Omega$ )	Rated Current				
Nominal	80						
Minimum	60						
Maximum	100	0.30	500 mA				

NOTES: UNLESS OTHERWISE SPECIFIED

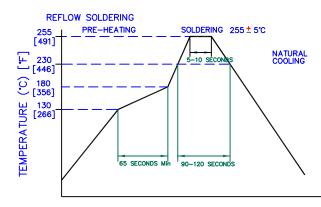
- 1. TAPED AND REELED per CURRENT EIA SPECIFICATIONS 7" REELS, 2,000 PCS/REEL.
- 2. TERMINATION FINISH IS 100% TIN.
- 3. COMPONENTS SHOULD BE ADEQUATELY PREHEATED BEFORE SOLDERING.
- 4. OPERATING TEMP. RANGE: -40°C~+125°C. (INCLUDING SELF-HEATING)

#### LAND PATTERNS FOR REFLOW SOLDERING



(For wave soldering, add 0.762 [.030] to this dimension.)

### RECOMMENDED SOLDERING CONDITIONS



		Z  , R,	AND X vs.	FREQUENCY	
	140				
IMPEDANCE (Ω)	120				
	100				
	80				
	60		<u> </u>	2	
	40		X.		
	20				
	0				
	1	10	100	1000	10000

FREQUENCY (MHz)

AGILENT E4991A RF Impedance/Material Analyzer HP 16194A Test Fixture. TEST REF. 3298

FREQUENCY (MHz)

	DIMENSIONS ARE IN mm [INCHES].			This print is the property of Lair	rd				
				Tech, and is loaned in confidence subject to return upon request of			_	=	T.
F	ADD OPERATING TEMPERATURE UPDATE LAIRD LOGO AND REFLOW CURVE	08/05/13	QU	subject to return upon request and with the understanding that no copies shall be made without the written consent of Laird Tech. All					
Ε	CHANGE REEL QTY FROM 3K TO 2K	11/05/08		rights to design or invention are		_			
D	UPDATE COMPANY LOGO	07/24/08	JRK	reserved.					
С	CORRECT REF # FROM 3289 TO 3298	07/22/05	JRK	PROJECT/PART NUMBER:	1	REV	PART TO	PE:	DRAWN BY:
	CORRECT LANDPATTERN DIM AND TEMP	04/14/03	JRK	LI1806E800R-10		F	CO-FIRE		JRK
-	IN REFLOW SOLDERING, ADD DC CURVE	04/14/03		DATE: 08 /07 /02		.E: N	L—— TS	SHEET:	
Α	ORIGINAL DRAFT	08/07/02	JRK	DATE: 08/07/02	TOOL		13	_	
RE	V DESCRIPTION	DATE	INT	LI1806E800R-10-F	_		2	of 2	